

<b>Notice of References Cited</b>	Application/Control No. 09/892,519	Applicant(s)/Patent Under Reexamination MAGNUSSEN ET AL.	
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#### U.S. PATENT DOCUMENTS

* A-M	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-2002/0065924 A1	05-2002	Barrall et al.	709/227
B	US-5,291,480 A	03-1994	Nimon, Kimmy F.	370/400
C	US-5,060,140	10-1991	Brown et al.	710/105
D	US-4,962,463	10-1990	Crossno et al.	715/500.1
E	US-6,591,310 B1	07-2003	Johnson, Stephen B.	710/3
F	US-2002/0026502	02-2002	Phillips et al.	709/219
G	US-6,791,989 B1	09-2004	Steinmetz et al.	370/412
H	US-2002/0001313	01-2002	Benayoun et al.	370/466
I	US-5,682,552	10-1997	Kuboki et al.	710/52
J	US-5,058,110	10-1991	Beach et al.	370/464
K	US-5,519,690	05-1996	Suzuka et al.	370/395.3
L	US-6,404,752	06-2002	Allen et al.	370/335
M	US-6,529,945	03-2003	Calhoun et al.	709/213

#### FOREIGN PATENT DOCUMENTS

* N-T	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

#### NON-PATENT DOCUMENTS

* U-X	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	Kanuma et al, "A CMOS 510K-Transistor Single-Chip Token-Ring LAN Controller (TRC) Compatible with IEEE802.5 MAC Protocol", IEEE, 1990, Pages 132-141.
V	Matsuda et al, "A new protocol processing architecture for high-speed networks", IEEE, 1996, Pages 798-803.
W	Camarda et al, "Performance evaluation of TCP/IP protocol implementations in end systems", IEEE, 1999, Pages 32-40.
X <sup>c</sup>	Khan et al, "An Asynchronous Communication Protocol for Heterogeneous Digital Signal Processing Systems", IEEE, 1995, Pages 761-764.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,633,866 B2	10-2003	Kishigami, Tohru	707/3
	B	US-6,766,381	07-2004	Barker et al.	709/246
	C	US-2003/0043825 A1	03-2003	Magnussen et al.	370/401
	D	US-2003/0126192 A1	07-2003	Magnussen, Andreas	709/202
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tuan et al, "RECONFIGURABLE PLATFORM DESIGN FOR WIRELESS PROTOCOL PROCESSORS", IEEE, 04/2001, Page 893-896.
	V	Chen et al, "A Systematic Design of Protocol Processors", IEEE, 1996, Pages 1034-1038.
	W	KONSTANTOULAKIS et al, "A Novel Architecture for Efficient Protocol Processing in High Speed Communication Environments", IEEE, 2000, Pages 425-432.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.